

Parallel Tensor Train Rounding using Gram SVD

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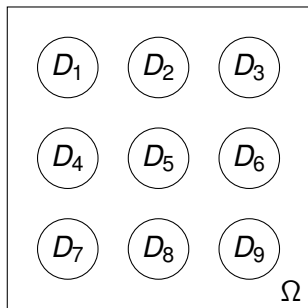
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Tensor Train (TT) can make very high dimensional problems tractable



Consider the parameter-dependent PDE:

$$\begin{aligned} -\operatorname{div}(\sigma(x, y; \rho) \nabla(u(x, y; \rho))) &= f(x, y) && \text{in } \Omega, \\ u(x, y; \rho) &= 0 && \text{on } \partial\Omega, \end{aligned}$$

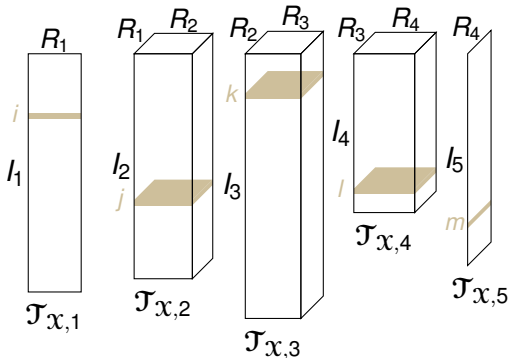
where σ is defined as:

$$\sigma(x, y; \rho) = \begin{cases} 1 + \rho_i & \text{if } (x, y) \in D_i \\ 1 & \text{elsewhere} \end{cases}$$

known as *cookies* problem [Tob12]

- Solving for all parameter values simultaneously, u is 11-D
- With mild assumptions, solution u has low TT ranks
- TT-based iterative linear solver exploits low-rank structure
 - can solve problem for high resolution [Dol13]

Tensor Train (TT) Notation



$$\mathbf{X} \approx \{\mathcal{T}_{\mathbf{X},n}\}, \mathbf{X} \in \mathbb{R}^{l_1 \times l_2 \times l_3 \times l_4 \times l_5}$$

$$\mathcal{T}_{\mathbf{X},n} \in \mathbb{R}^{R_{n-1} \times l_n \times R_n}$$

are *TT cores*

$$x_{ijklm} \approx \sum_{\alpha=1}^{R_1} \sum_{\beta=1}^{R_2} \sum_{\gamma=1}^{R_3} \sum_{\delta=1}^{R_4} \mathcal{T}_{\mathbf{X},1}(i, \alpha) \mathcal{T}_{\mathbf{X},2}(\alpha, j, \beta) \mathcal{T}_{\mathbf{X},3}(\beta, k, \gamma) \mathcal{T}_{\mathbf{X},4}(\gamma, l, \delta) \mathcal{T}_{\mathbf{X},5}(\delta, m)$$

Given a tensor in TT format, often need to compress the ranks

- algebraic operations on TT formats over-extend ranks
- recompression (rank truncation) subject to error threshold
 - or subject to target ranks
- analogous to floating point rounding

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- analogous to floating point rounding

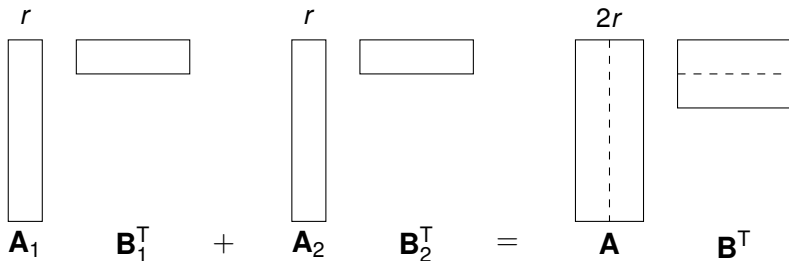
Goal: compute truncated SVDs of matricized TT-format tensors

- TT-ranks are ranks of unfoldings $\mathbf{X}_{(1:n)}$ for $1 \leq n \leq N$
- if \mathcal{X} is $l_1 \times l_2 \times \cdots \times l_N$, then $\mathbf{X}_{(1:n)}$ is $(l_1 \cdots l_n) \times (l_{n+1} \cdots l_N)$
- $\mathbf{X}_{(1:n)}$ is generally a huge matrix, but it is highly structured

2D TT-Rounding (Matrix Case)

Low-rank matrix addition example

- consider $\mathbf{A}_1 \mathbf{B}_1^T + \mathbf{A}_2 \mathbf{B}_2^T$, where each factor has r columns
- can represent this in low-rank format $[\mathbf{A}_1 \ \mathbf{A}_2] [\mathbf{B}_1 \ \mathbf{B}_2]^T$ which now has rank $2r$
- goal is to compute low-rank approximation with rank $k < 2r$



Matrix Rounding via QR Decompositions

QR-based algorithm for rounding rank- r matrix $\mathbf{X} = \mathbf{A}\mathbf{B}^T$:

function $[\mathbf{U}, \mathbf{V}] = \text{QR-ROUNDING}(\mathbf{A}, \mathbf{B}, k)$

$[\mathbf{Q}_A, \mathbf{R}_A] = \text{QR}(\mathbf{A})$ \triangleright (tall-skinny) QR decomposition

$[\mathbf{Q}_B, \mathbf{R}_B] = \text{QR}(\mathbf{B})$ \triangleright (tall-skinny) QR decomposition

$[\hat{\mathbf{U}}_R, \hat{\Sigma}_R, \hat{\mathbf{V}}_R] = \text{TSVD}(\mathbf{R}_A \mathbf{R}_B^T, k)$ \triangleright k th truncated SVD

$\mathbf{U} = \mathbf{Q}_A \hat{\mathbf{U}}_R$

$\mathbf{V} = \mathbf{Q}_B (\hat{\mathbf{V}}_R \hat{\Sigma}_R)$ $\triangleright \mathbf{A}\mathbf{B}^T \approx \mathbf{U}\mathbf{V}^T$

Here's the algebra:

$$\mathbf{A}\mathbf{B}^T = \underbrace{\mathbf{Q}_A \mathbf{R}_A}_{\mathbf{A}} \underbrace{\mathbf{R}_B^T \mathbf{Q}_B^T}_{\mathbf{B}^T} \approx \mathbf{Q}_A \underbrace{\hat{\mathbf{U}}_R \hat{\Sigma}_R \hat{\mathbf{V}}_R^T}_{\mathbf{R}_A \mathbf{R}_B^T} \mathbf{Q}_B^T = \underbrace{\mathbf{U}}_{\mathbf{Q}_A \hat{\mathbf{U}}_R} \underbrace{\mathbf{V}^T}_{\hat{\Sigma}_R \hat{\mathbf{V}}_R^T \mathbf{Q}_B^T}$$

Matrix Rounding via Gram (1st attempt)

Gram SVD is a well-known method for computing singular values of rectangular matrices

- sing. values of \mathbf{X} are square roots of eigenvalues of $\mathbf{X}^T\mathbf{X}$
- if \mathbf{X} is tall and skinny then $\mathbf{X}^T\mathbf{X}$ is much smaller
- sacrifices some accuracy of singular values and vectors

If $\mathbf{X} = \mathbf{A}\mathbf{B}^T$, then we can try direct application:

- $\mathbf{X}^T\mathbf{X} = \mathbf{B}(\mathbf{A}^T\mathbf{A})\mathbf{B}^T$
- $\mathbf{X}\mathbf{X}^T = \mathbf{A}(\mathbf{B}^T\mathbf{B})\mathbf{A}^T$

but we still have to orthogonalize one of the two factors in order to compute the eigendecomposition

Matrix Rounding via Gram SVD

Gram-based algorithm for rounding rank- r matrix $\mathbf{X} = \mathbf{AB}^T$:

function $[\mathbf{U}, \mathbf{V}] = \text{GRAM-ROUNDING}(\mathbf{A}, \mathbf{B}, k)$

$\mathbf{G}_A = \mathbf{A}^T \mathbf{A}$ ▷ symmetric matrix multiplication

$\mathbf{G}_B = \mathbf{B}^T \mathbf{B}$ ▷ symmetric matrix multiplication

$[\mathbf{V}_A, \Lambda_A] = \text{EIG}(\mathbf{G}_A)$

$[\mathbf{V}_B, \Lambda_B] = \text{EIG}(\mathbf{G}_B)$

$[\hat{\mathbf{U}}, \hat{\Sigma}, \hat{\mathbf{V}}] = \text{TSVD}(\Lambda_A^{1/2} \mathbf{V}_A^T \mathbf{V}_B \Lambda_B^{1/2}, k)$ ▷ k th truncated SVD

$\mathbf{U} = \mathbf{A} \left(\mathbf{V}_A \Lambda_A^{-1/2} \hat{\mathbf{U}} \right)$

$\mathbf{V} = \mathbf{B} \left(\mathbf{V}_B \Lambda_B^{-1/2} \hat{\mathbf{V}} \hat{\Sigma} \right)$ ▷ $\mathbf{AB}^T \approx \mathbf{UV}^T$

Matrix Rounding via Gram SVD (continued)

Here's the algebra:

$$\begin{aligned} \mathbf{AB}^T &= \underbrace{\mathbf{U}_A \Sigma_A \mathbf{V}_A^T}_{\mathbf{A}} \underbrace{\mathbf{V}_B \Sigma_B \mathbf{U}_B^T}_{\mathbf{B}^T} \\ &= \underbrace{\mathbf{A} \mathbf{V}_A \Lambda_A^{-1/2} \Lambda_A^{1/2} \mathbf{V}_A^T}_{\mathbf{A}} \underbrace{\mathbf{V}_B \Lambda_B^{1/2} \Lambda_B^{-1/2} \mathbf{V}_B^T}_{\mathbf{B}^T} \\ &= \underbrace{(\mathbf{A} \mathbf{V}_A \Lambda_A^{-1/2})}_{\mathbf{U}_A} \underbrace{\Lambda_A^{1/2} \mathbf{V}_A^T \mathbf{V}_B \Lambda_B^{1/2}}_{\mathbf{M}} \underbrace{(\mathbf{B} \mathbf{V}_B \Lambda_B^{-1/2})^T}_{\mathbf{U}_B} \\ &\approx \underbrace{(\mathbf{A} \mathbf{V}_A \Lambda_A^{-1/2})}_{\mathbf{U}_A} \hat{\mathbf{U}} \hat{\Sigma} \hat{\mathbf{V}}^T \underbrace{(\mathbf{B} \mathbf{V}_B \Lambda_B^{-1/2})^T}_{\mathbf{U}_B} \\ &= \underbrace{\mathbf{A} (\mathbf{V}_A \Lambda_A^{-1/2} \hat{\mathbf{U}})}_{\mathbf{U}} \underbrace{(\hat{\Sigma} \hat{\mathbf{V}}^T \Lambda_B^{-1/2} \mathbf{V}_B^T)}_{\mathbf{V}^T} \mathbf{B}^T \end{aligned}$$

QR-Rounding vs Gram-Rounding

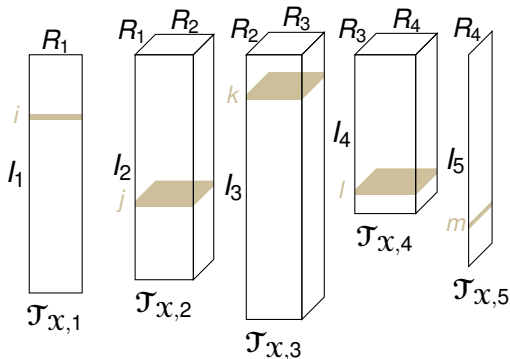
Gram-Rounding is at least twice as fast as QR-Rounding

- Gram-Rounding does half the flops of QR-Rounding
- Gram-Rounding dominated by matrix multiplication
- QR-Rounding dominated by computing QR and applying Q

QR-Rounding is more accurate than Gram-Rounding

- QR-Rounding computes singular values as small as $\sigma_1 \cdot \varepsilon$
- Gram-Rounding computes singular values only to $\sigma_1 \cdot \varepsilon^{1/2}$

Tensor Train (TT) Notation



$$\mathbf{X} \approx \{\mathcal{J}_{\mathbf{X},n}\}, \mathbf{X} \in \mathbb{R}^{l_1 \times l_2 \times l_3 \times l_4 \times l_5}$$

$$\mathcal{J}_{\mathbf{X},n} \in \mathbb{R}^{R_{n-1} \times l_n \times R_n}$$

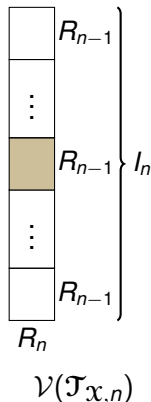
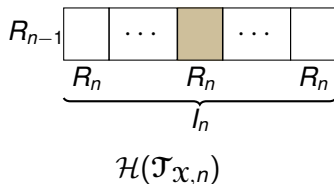
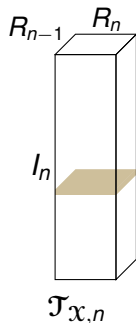
are *TT cores*

$$x_{ijklm} \approx \sum_{\alpha=1}^{R_1} \sum_{\beta=1}^{R_2} \sum_{\gamma=1}^{R_3} \sum_{\delta=1}^{R_4} \mathcal{J}_{\mathbf{X},1}(i, \alpha) \mathcal{J}_{\mathbf{X},2}(\alpha, j, \beta) \mathcal{J}_{\mathbf{X},3}(\beta, k, \gamma) \mathcal{J}_{\mathbf{X},4}(\gamma, l, \delta) \mathcal{J}_{\mathbf{X},5}(\delta, m)$$

Important core unfoldings

$\mathcal{H}(\mathcal{J}_{x,n}) \in \mathbb{R}^{R_{n-1} \times I_n R_n}$ and $\mathcal{V}(\mathcal{J}_{x,n}) \in \mathbb{R}^{R_{n-1} I_n \times R_n}$

are horizontal and vertical unfoldings of n th core



QR-Based TT Rounding Algorithm [Ose11]

function $\{\mathcal{J}_{\mathbf{z},n}\} = \text{TT-ROUNDING}(\{\mathcal{J}_{\mathbf{x},n}\})$

▷ **Orthogonalization Phase**

for $n = N$ **down to** 2 **do**

$$[\mathbf{Y}_n, \mathbf{R}_n] = \text{QR}(\mathcal{H}(\mathcal{J}_{\mathbf{x},n})^T)$$

$$\mathcal{V}(\mathcal{J}_{\mathbf{x},n-1}) = \mathcal{V}(\mathcal{J}_{\mathbf{x},n-1}) \cdot \mathbf{R}^T$$

▷ (tall-skinny) QR factorization

▷ Apply \mathbf{R} to previous core

▷ **Truncation Phase**

$$\mathbf{z} = \mathbf{x}$$

for $n = 1$ **to** $N - 1$ **do**

$$[\mathbf{Y}_n, \mathbf{R}_n] = \text{QR}(\mathcal{V}(\mathcal{J}_{\mathbf{z},n}))$$

$$[\hat{\mathbf{U}}_R, \hat{\Sigma}, \hat{\mathbf{V}}] \approx \text{TSVD}(\mathbf{R}_n)$$

$$\mathcal{V}(\mathcal{J}_{\mathbf{z},n}) = \text{APPLY-Q}(\mathbf{Y}_n, \hat{\mathbf{U}}_R)$$

$$\mathcal{H}(\mathcal{J}_{\mathbf{z},n+1})^T = \text{APPLY-Q}(\mathbf{Y}_{n+1}, \hat{\mathbf{V}}\hat{\Sigma})$$

▷ (tall-skinny) QR factorization

▷ Truncated SVD of \mathbf{R}

▷ Form explicit $\hat{\mathbf{U}}$

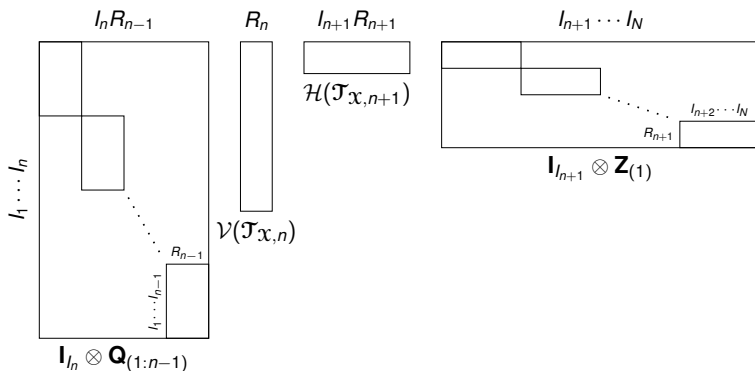
▷ Apply $\hat{\Sigma}\hat{\mathbf{V}}^T$ to next core

We have parallelized this QR-based algorithm [DBB22] using the parallel TSQR algorithm [DGHL12]

More details on TT-Rounding...

TT-Rounding does truncated SVDs on $\mathbf{X}_{(1)}$, $\mathbf{X}_{(1:2)}$, $\mathbf{X}_{(1:3)}$, etc., and here is the matrix expression of each unfolding [DBB22]:

$$\mathbf{X}_{(1:n)} = (\mathbf{I}_{l_n} \otimes \mathbf{Q}_{(1:n-1)}) \cdot \mathcal{V}(\mathcal{T}_{x,n}) \cdot \mathcal{H}(\mathcal{T}_{x,n+1}) \cdot (\mathbf{I}_{l_{n+1}} \otimes \mathbf{Z}_{(1)})$$



Main Ideas of Gram-Based TT-Rounding

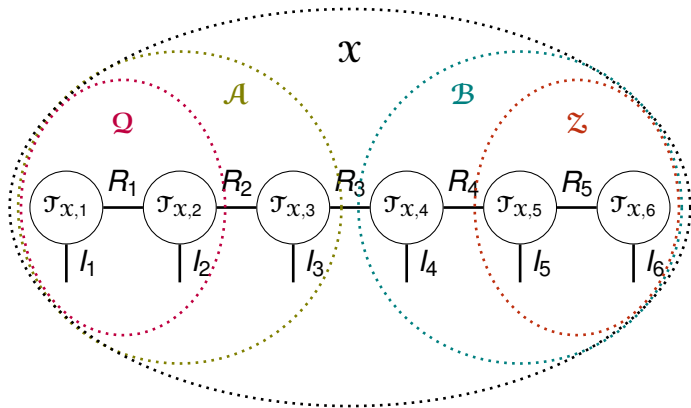
Same matrix expression of n th unfolding:

$$\mathbf{X}_{(1:n)} = \underbrace{(\mathbf{I}_{I_n} \otimes \mathbf{Q}_{(1:n-1)}) \cdot \mathcal{V}(\mathcal{J}x_n)}_{\mathbf{A}} \cdot \underbrace{\mathcal{H}(\mathcal{J}x_{n+1}) \cdot (\mathbf{I}_{I_{n+1}} \otimes \mathbf{Z}_{(1)})}_{\mathbf{B}^T}$$

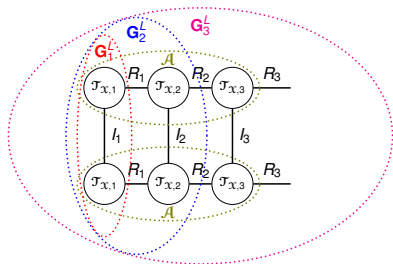
- just like matrix case, we need to compute $\mathbf{A}^T \mathbf{A}$ and $\mathbf{B}^T \mathbf{B}$
- two key differences:
 - \mathbf{A} and \mathbf{B} are both highly structured
 - we need Gram matrices for all $1 \leq n \leq N$
- we obtain efficiency by exploiting structure and by exploiting the computational overlap across modes

Tensor Network Diagram for TT

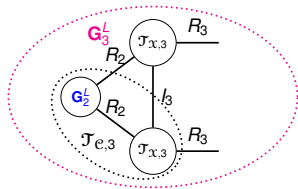
$$\mathbf{X}_{(1:n)} = \underbrace{(\mathbf{I}_{l_n} \otimes \mathbf{Q}_{(1:n-1)}) \cdot \mathcal{V}(\mathcal{J}_{x,n})}_{\mathbf{A}} \cdot \underbrace{\mathcal{H}(\mathcal{J}_{x,n+1}) \cdot (\mathbf{I}_{l_{n+1}} \otimes \mathbf{Z}_{(1)})}_{\mathbf{B}^T}$$



Tensor Network Diagram for Gram Matrix Computation

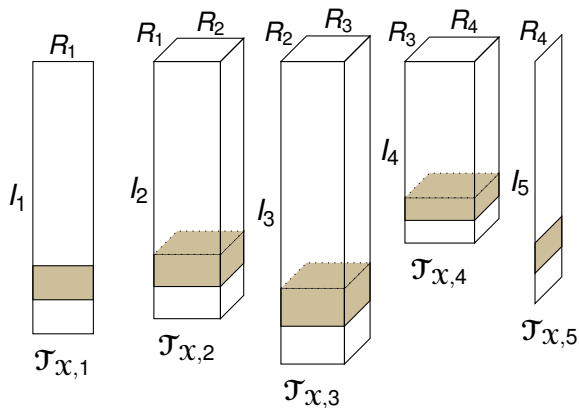


tensor network for $\mathbf{G}_3^L = \mathbf{A}^T \mathbf{A}$



intermediate step,
computing \mathbf{G}_3^L from \mathbf{G}_2^L

Parallel Distribution

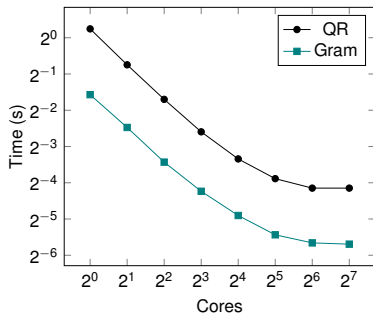


- Each core distributed across all P processors
- Local n th core dimensions are $R_{n-1} \times \frac{l_n}{P} \times R_n$
- Key: vertical and horizontal unfoldings are 1D-distributed

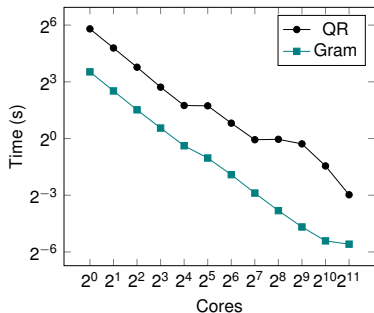
Strong Scaling Results for Synthetic Tensors

We round synthetic tensors with input TT-ranks $R = 20$ (all modes) down to $R = 10$, scaling up the number of processors

- results on Andes (ORNL), with 2 16-core AMD EPYC procs per node



$N=50, 2K \times \dots \times 2K$ (77 MB)

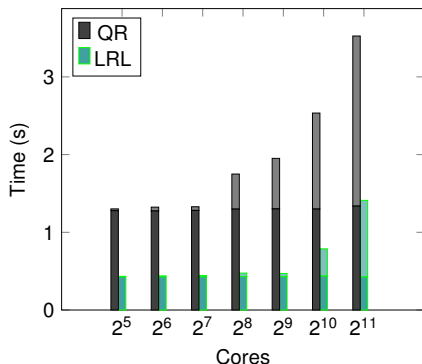


$N=16, 100M \times 50K \times \dots \times 50K \times 1M$ (8 GB)

Weak Scaling Results for Synthetic Tensor

We round the 50-mode synthetic tensor with input TT-ranks $R = 20$ down to $R = 10$, scaling up processors with fixed local data

- results on Andes (ORNL), with 2 16-core AMD EPYC procs per node

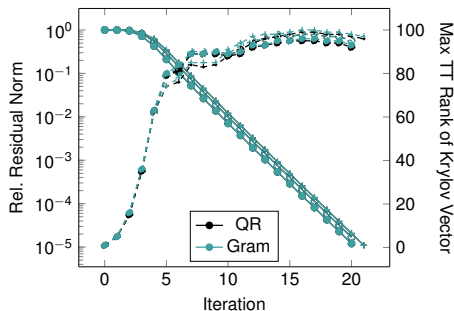
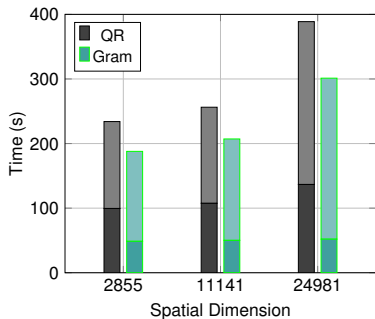


Dark signifies computation, light signifies communication

Results for Cookies Problem

We solve the Cookies problem with Matlab implementation of TT-GMRES [DoI13] and TT-Rounding (sequential)

- we use 4 parameters (cookies) and vary spatial discretization



Gram has negligible effect on GMRES behavior

Dark signifies time in TT-Rounding

- TT format efficiently approximates high-dim. tensors
 - TT arithmetic causes rank growth, TT-Rounding is key
 - enables solving problems like parameter-dependent PDEs
- TT-Rounding means truncated SVDs of unfoldings $\mathbf{X}_{(1:n)}$
 - QR-based approach most accurate but less efficient
 - Gram-based approach faster, can be accurate enough
- Gram-based computation exploits TT structure
 - efficient contraction of tensor network
 - par. distribution allows for comm.-efficient matrix multiplies

Thanks for your attention!

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MPI_ATTAC: Algorithms for Tensor Train Arithmetic and Computations

https://gitlab.com/aldaas/mpi_attac

Hussam Al Daas, Grey Ballard, and Lawton Manning.

Parallel Tensor Train Rounding using Gram SVD.

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Hussam Al Daas, Grey Ballard, and Peter Benner.

Parallel Algorithms for Tensor Train Arithmetic.

SIAM Journal on Scientific Computing 2022.

<https://dx.doi.org/10.1137/20M1387158>

More details on TT-Rounding...

TT-Rounding does SVDs on $\mathbf{X}_{(1)}$, $\mathbf{X}_{(1:2)}$, $\mathbf{X}_{(1:3)}$, etc., so we seek similar matrix expressions of those unfoldings

The unfolding of \mathcal{X} that maps the first n tensor dimensions to rows can be expressed as a product of four matrices:

$$\mathbf{X}_{(1:n)} = (\mathbf{I}_n \otimes \mathbf{Q}_{(1:n-1)}) \cdot \mathcal{V}(\mathcal{X}_{n,n}) \cdot \mathcal{H}(\mathcal{X}_{n,n+1}) \cdot (\mathbf{I}_{n+1} \otimes \mathbf{Z}_{(1)})$$

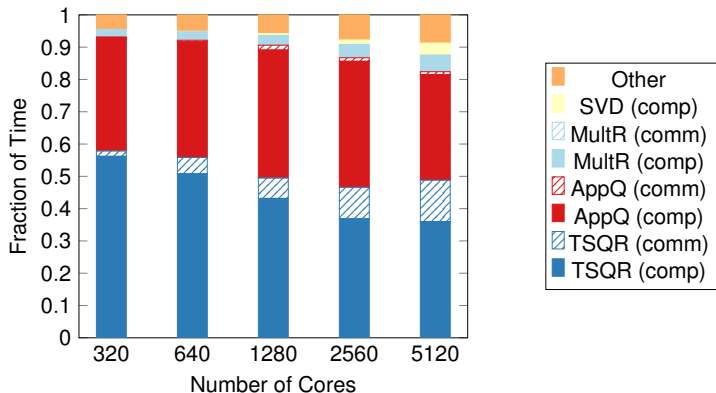
where \mathbf{Q} is $I_1 \times \dots \times I_{n-1} \times R_{n-1}$ with

$$\mathcal{Q}(i_1, \dots, i_{n-1}, r_{n-1}) = \mathcal{X}_{1,1}(i_1, :) \cdot \mathcal{X}_{2,2}(:, i_2, :) \cdots \mathcal{X}_{n-1,n-1}(:, i_{n-1}, r_{n-1}),$$

and \mathbf{Z} is $R_{n+1} \times I_{n+2} \times \dots \times I_N$ with

$$\mathcal{Z}(r_{n+1}, i_{n+2}, \dots, i_N) = \mathcal{X}_{n+2,n+2}(r_{n+1}, i_{n+2}, :) \cdot \mathcal{X}_{n+3,n+3}(:, i_{n+3}, :) \cdots \mathcal{X}_{N,N}(:, i_N).$$

Time Breakdown of Parallel QR-based TT-Rounding



- TT tensor: $I_n = 512K$, $R_n = 60 \rightarrow 30$, $N = 50$
- 70-80% of time spent in QR computations



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